

ABSTRACT OF THE DISCLOSURE

A power-on reset circuit and method for generating a reset signal according to the voltage of a power source. The circuit includes an oscillator for generating an oscillation signal. The frequency of the oscillation signal increases with the rising
5 of the voltage of the power source. The circuit further includes a frequency detector for converting the oscillation frequency of the oscillation signal into a first output voltage, and includes a reset signal output circuit for outputting a reset signal according to the first output voltage. Therefore, the power-on reset circuit can be applied in low-voltage chips.